

## RESPONSE UNDER 37 CFR 1.116 -EXPEDITED PROCEDUREEXAMINING GROUP ART UNIT 2183

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

Leber et al.

Examiner: Kevin P. Rizzuto

Serial No:

09/683,351

Group Art Unit: 2183

Filed:

February 18, 2001

Docket: DE920000097US1 (8728-729)

For:

METHOD FOR HANDLING 32 BIT RESULTS FOR AN OUT-OF-

ORDER PROCESSOR WITH A 64 BIT ARCHITECTURE

Mail Stop AF Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450

## AMENDMENT UNDER 37 C.F.R. § 1.116

Sir:

This is a response to the Final Office Action mailed June 8, 2005. Please amend the above-identified application as follows:

## CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, postpaid in an envelope, addressed to: Mail Stop AF, Commissioner for Patents, PO Box 1450, Alexandria VA, 22313-1450 on <u>August 8, 2005</u>.

Dated: <u>7-8 -</u>

Koon Hon Wong